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 Parallel Architectures and Compilation Techniques, 1998. Proceedings. 1998 International Conference on , 1998
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 Frontiers in Education Conference, 1996. FIE '96. 26th Annual Conference., Proceedings of , Volume: 2 , 1996
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Findlay, P.A.; Trainis, S.A.; Steven, G.B.; Adams, R.G.
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 Page(s): 368 -372

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6 ADAing and ABETing TPS portability

Walters, M.D.; Cundiff, P.A.

AUTOTESTCON '91. IEEE Systems Readiness Technology Conference. Improving Systems Effectiveness in the Changing Environment of the '90s, Conference Record. , 1991

Page(s): 193 -198

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7 Correctness proofs of parameterized hardware modules in the Cathedral-II synthesis environment

Verkest, D.; Claesen, L.; De Man, H.

Design Automation Conference, 1990., EDAC. Proceedings of the European , 1990

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8 Improving cache performance by selective cache bypass

Chi, C.-H.; Dietz, H.

System Sciences, 1989. Vol.I: Architecture Track, Proceedings of the Twenty-Second Annual Hawaii International Conference on , 1989

Page(s): 277 -285 vol.1

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